

SEMICONDUCTOR APPARATUS

## ABSTRACT

5           A semiconductor apparatus for flexibly and  
effectively configuring a delay monitor circuit while  
suppressing an increase of the circuit scale to minimum,  
comprising a delay signal generation circuit for  
switching the configuration of delay element arrays based  
10 on first configuration information and second  
configuration information and propagating a delay element  
array wherein a pulse is switched, a register group  
having a first register to be set the first configuration  
information and a second register to be set second  
15 configuration information, a selector for outputting to  
the delay signal generation circuit the first  
configuration information and second configuration  
information in accordance with an instruction of a  
selection signal in a time sharing way, and a control  
20 circuit for controlling a power source voltage based on  
delay information of a delay element array and outputting  
to the selector a selection signal to select from the  
first configuration information and second configuration  
information in a time sharing way.